

RTVCS-2026

TWO-DAY INTERNATIONAL WORKSHOP ON Recent Trends in VLSI, Circuits and Systems

30–31 October, 2026

Under the Aegis of

7th International Conference on Micro/Nanoelectronics Devices, Circuits and Systems

MNDCS-2027

*Jointly Organized by the Department of Electronics & Telecommunication Engineering,
Department of Computer Science & Engineering, Assam Engineering College, and
Department of Electronics and Communication Engineering, National Institute of
Technology, Silchar in association with IEEE CAS Kolkata Section*

DAY 1 | Friday, 30 October 2026

TIME	SESSION / SPEAKER
8:00–9:00 AM	Registration
9:00–10:00 AM	Inauguration Ceremony and Keynote Talk 1 — Ms. Vaswatii Sarmah Barthakur <i>Head – Assam Regional Strategy & Ecosystem Development TSAT; Presiding Officer – POSH, TSAT Assam</i>
10:00–10:30 AM	High Tea
10:30–11:10 AM	Keynote Talk 2 — Prof. Gaurav Trivedi <i>Professor, Department of EEE & Mehta Family School of Data Science and Artificial Intelligence, IIT Guwahati</i>
11:15–11:55 AM	Invited Talk 1 — Dr. Koushik Guha <i>Associate Professor & Head, Department of ECE, NIT Silchar</i>
12:00–12:40 PM	Invited Talk 2 — Dr. Trupti Ranjan Lenka <i>Associate Professor, Department of ECE, NIT Silchar</i>
12:45–1:25 PM	Invited Talk 3 — Dr. Chandrima Mondal <i>Assistant Professor, Department of ETE, Jadavpur University; Vice-Chair, IEEE CAS Kolkata Section</i>
1:25–2:25 PM	Lunch
2:25–2:55 PM	Invited Talk 4 — Mr. Salil Nath <i>Regional Head, NER, Telecom Sector Skill Council</i>
3:00–3:30 PM	Invited Talk 5 — Mr. Arnab Kumar Barman <i>CEO & Mission Director, Da Spatio Rhobotique Laboratory</i>
3:30–3:40 PM	Tea Break
3:40–4:20 PM	Keynote Talk 3 — Prof. Ricardo Augusto da Luz Reis <i>Professor, Federal University of Rio Grande do Sul (UFRGS), Brazil</i>
4:20–5:00 PM	Discussion and Networking

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DAY 2 | Saturday, 31 October 2026

TIME	SESSION / SPEAKER
9:00–9:30 AM	Invited Talk 6 — Mr. Kavinga Upul Ekanayake <i>Head of Hardware Engineering, ACCELR, Sri Lanka</i>
9:30–9:40 AM	Tea Break
9:40–1:30 PM	Parallel Hands-on Session I — RTL Coding & FPGA Implementation <i>Verilog HDL coding, functional verification, synthesis, constraints, debugging and testing</i>
9:40–1:30 PM	Parallel Hands-on Session II — Semi-Custom IC Design Flow <i>Logic equivalence checking, floor/power planning, placement, clock-tree synthesis, routing, static timing analysis and GDSII generation</i>
1:30–2:15 PM	Lunch
2:15–2:45 PM	Keynote Talk 4 — Dr. Prateek Sikka <i>Senior Group Manager, Systems Architecture, Microcontrollers and Digital RF Group, STMicroelectronics Pvt. Ltd.</i>
2:45–3:25 PM	Invited Talk 7 — Prof. Sergio Duarte Correia <i>Professor, Technology Department, Portalegre Polytechnic University, Portugal</i>
3:30–4:30 PM	Parallel Hands-on Session I — RTL Coding & FPGA Implementation
3:30–4:30 PM	Parallel Hands-on Session II — Semi-Custom IC Design Flow
4:30–5:00 PM	Valedictory Ceremony
5:00 PM onwards	Networking over Tea